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#### Simics: A full system simulation platform

PS Magnusson, M Christensson, J Eskilson, D ... - COMPUTER., 2002 - computer.org

... write **breakpoints** can check lock semantics around data structures, and timing **breakpoints** can trigger ... In addition, the **testing** can be automated by using checkpoints and scripts, and ... no side effects, such as an alignment exception, TLB miss, cache miss, or **simulator breakpoint**. ...

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#### [PDF] A Myrinet Firmware development experience

M Herbert - 2002 - Citeseer

... a running kernel, since this allows very fast **testing**: no need to reboot to **test** a new **driver** (unless a fatal **error** freezes the ... The **test** starts ... Traffic **simulation** As every type of host- LAN ai communication, the **simulation** of the incoming and outgoing Ethernet traffic goes through the ...

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psu.edu [PDF]

#### [PDF] Targeted software fault insertion

P Houlihan - Proceedings of STAR EAST, 2001 - mangosoft.com

... wanted to fail all I/O, this technique would result in a lot of **breakpoints** firing and ... Once the **breakpoint** is removed, the knowledge of the most effective point for fault insertion is lost ... There is no need for tedious manipulation of the **test** environment and no time consuming delays in ...

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mangosoft.com [PDF]

#### METHOD FOR PROGRAM DEBUGGING

HC Lien, HY Tseng, YC Huang, KP ... - US Patent App. 10/ ..., 2004 - Google Patents

... Diagnosis code **Test** 1st **breakpoint** 2nd **breakpoint** 00h 01h CMOS **test** Memory **test** N-th **breakpoint** N-th Hardware **test** [0023] The interrupt nature of **breakpoints** means a soft ... This causes the execution of memory **test** module to turn into the path of **error handler** "no memory". ...

#### [PDF] A structured approach to embedded control systems implementation

JF Broenink, GH Hilderink - 2001 IEEE International Conference on ..., 2001 - Citeseer

... But even worse, there is a **break point** in the way of working: the results of the previous design ... Design and **test** protocols on machine level (eg homing to ensure proper repeatability). ... Impact of these additions on the behavior of the ECS can be checked by means of **simulation**. ...

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psu.edu [PDF]

#### [PDF] Some efficient architecture simulation techniques

R Bedichek - Winter 1990 Usenix Conference, 1990 - Citeseer

... Separating translation buffers into four sections reduces the amount of **testing** and branching that memory ... The **simulator** is much more forgiving; it prints an explanation and stops on the ... These sequences of code cannot be single stepped or contain **breakpoints** on the real ...

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#### [PDF] From Formal Specification to Implementation: Experience with Protocol Tools

TJ Baumgartner, DM Kristol, JD Milleville, ... - ... Conference on Network ..., 1993 - ieee-icnp.org

... for developers. Example capabilities include set- ting **breakpoints**, function tracing, and inserting data. With this **simulator**, we debugged critical call flows (**test** sequences) along with the 5ESS call processing applica- tion. In our ...

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#### An experiment in software engineering: The architecture research facility as a case study

HS Eliovitz - Proceedings of the 4th international conference on ..., 1979 - portal.acm.org

... A user may install a **simulation** from a file9 start the **simulation** at any labeled point in the ISP description9 set and clear **break- point** conditions that specify when a **simulation** will be interrupted automatically ... During the **testing** of the RTM **simulator** module, approximately ten ...

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#### [PDF] JN: An Operating System for an Embedded JavaNetworkComputer

BR Montague - IEEE Micro, 1997 - Citeseer

... to get our first trivial Java program to run and produce output under the **simulator**. ... This port was significantly expedited by a suite of Na- tional hardware **test** and 'demonstration ... by the clock interrupt, rather than generating interrupts it- self, and there was no **driver error handling**. ...

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psu.edu [PDF]

#### HITS: A symbolic testing and debugging system for multilingual microcomputer software

T Chusho, A Tanaka, E Okamoto, A Honda, ... - ... of the May 16-19, 1983 ..., 1983 - portal.acm.org

... A **breakpoint** is expressed by the procedure names or statement numbers for the target ... For the target program in assembly language, a **break- point** is expressed by the label names ... time, temporary commands for debugging can be entered without rewriting the **test** procedure in ...

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